

Innovating Energy Technology

2MBI600XNH170-50

IGBT Modules

Power Module (X series) 1700V / 600A / 2-in-1 package

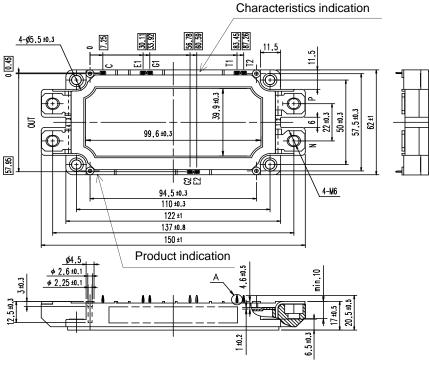
Features

Low $V_{CE(sat)}$ Low Inductance Module structure Press fit pin terminals

Applications

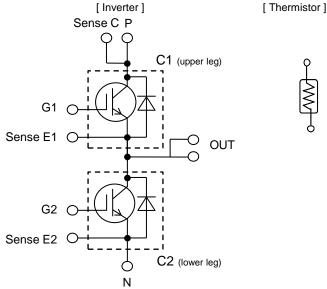
Inverter for Motor Drives, AC and DC Servo Drives Uninterruptible Power Supply Systems, Wind Turbines, PV Power Conditioning Systems

■ Outline drawing (Unit : mm)





Equivalent Circuit







Weight: 350 g(typ.)

Τ1

T2





■ Absolute Maximum Ratings (at T_c= 25°C unless otherwise specified)

		Items	Symbols	Cond	litions	Maximum Ratings	Units
	Collecto	r-emitter voltage, gate-emitter short-circuited	V _{CES}			1700	V
Gate-emitter voltage, collector-emitter short-circuited			V _{GES}			±20	V
	Collecto	r current	/ _c	Continuous	$T_{\rm C}$ =100°C	600	
	Repetitiv	e peak collector current	I _{CRM}	1ms		1200	
rter	Forward	current	/ _F			600	A
은 Forward current 온 Repetitive peak forward current		I _{FRM}	1ms		1200		
Total power dissipation		P _{tot}	1 device		3845	W	
	Virtual ju	unction temperature	T_{vj}			175	
Operating junction temperature			T _{vjop}			175	°C
(under switching conditions)							
Case temperature		T _c			125		
Sto	orage terr	perature	T_{stg}			-40 ~ 125	
Isolation between terminal and copper base (*1)		V _{isol}	AC: 1min.		3400	Vrms	
voltage between thermistor and others (*2)		V isol	AC. IIIIII.		3400	VIIIIS	
Mc	ounting to	rque of screws to heatsink (*3)	Ms	M5		6.0	N⋅m
Mc	ounting to	rque of screws to terminals (*3)	Μ _t	M6		6.0	'''''

(*1) All terminals should be connected together during the test.

(*2) Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

(*3)Recommendable Value:: Mounting torque of screws to heatsink $2.5 \sim 6.0 \text{ N·m}$ (M5)Recommendable Value:: Mounting torque of screws to terminals $3.5 \sim 6.0 \text{ N·m}$ (M6)



■ Electrical characteristics (at *T*_{vj}= 25°C unless otherwise specified)

liama		• • •	•	Characteristics				
	Items	Symbols	Conditio	ns	min.	typ.	max.	Units
	Collector-emitter cut-off current, gate-emitter short- circuited	I _{CES}	$V_{GE} = 0V$ $V_{CE} = 1700V$		-	-	150	μA
	Gate leakage current, collector-emitter short- circuited	I _{GES}	$V_{CE}=0V, V_{GE}=\pm 2$	0V	-	-	300	nA
	Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$V_{CE} = 20V$ $I_{C} = 600mA$		6.0	6.5	7.0	V
		V _{CE(sat)} (terminal)		T _{vj} =25°C	-	2.50	2.95	
	Collector-emitter		V _{GE} = 15V	T _{vj} =25°C	-	1.70	2.15	
	saturation voltage	V _{CE(sat)}	I _C = 600A	T _{vi} =125°C	-	2.10	-	- V
		(chip)		<i>T</i> _{vi} =150°C	-	2.25	-	_
				- <i>T</i> _{νi} =175°C	-	2.35	-	
	Internal gate resistance	r _g	-	1 01 110 0	-	1.67	-	Ω
		Cies			-	75	-	
	Capacitance	C _{oes}	V _{CE} =10V, V _{GE} =0	V. f=1MHz	-	2.1	-	nF
	Capacharico	Cres	CE^{-100} , CE^{-00} , CE^{-00} , CE^{-00} , CE^{-00}		-	0.47		- " '
	Gate charge	Q _G	$V_{\rm CC} = 900 \text{V}, I_{\rm C}$ $V_{\rm GE} = -15 \rightarrow +15$	-	4.7	-	μC	
ter	Forward voltage	V _F (terminal)	$V_{GE} = 0V$ $I_F = 600A$	T _{vj} =25°C	-	2.50	2.95	
nverter			-	T _{vi} =25°C	-	1.70	2.15	_
l		V _F		-, Τ _{vj} =125°C	-	1.80	-	- V
		(chip)		vj <i>T</i> _{vj} =150°C	-	1.80	-	_
				T _{vi} =175°C	-	1.80	-	_
			V _{CC} = 900V	$T_{vj}=170^{\circ}\text{C}$ $T_{vj}=25^{\circ}\text{C}$	-	0.49	-	_
	Switching time (*1)			$T_{vj}=125^{\circ}C$	-	0.43	-	
		t _{d(on)}	$V_{GE} = +15/-15 V$		-	0.61	-	
			$R_{\rm G} = \pm 1\Omega$	<i>T</i> _{vi} =175°C	-	0.62	-	_
			$L_{\rm S} = 35 \rm nH$	$T_{\rm vi}=25^{\circ}\rm C$	-	0.11	-	_
			23 - 00 111	T _{vi} =125°C	-	0.12	-	-
		t _r		$T_{vj}=150^{\circ}C$	-	0.13	-	_
				T _{vi} =175°C	-	0.14	-	_
		<u> </u>	1	T _{vi} =25°C	-	0.58	-	-
				T _{vi} =125°C	-	0.63	-	μs
		$t_{d(off)}$		T _{vi} =150°C	-	0.65	-	- '
				<i>T</i> _{vi} =175°C	-	0.66	-	
			1	T _{vj} =25°C	-	0.42	-	
		t _f		T _{vj} =125°C	-	0.61	-	
		-1		<i>T</i> _{vj} =150°C	-	0.66	-	_
			-	T _{vj} =175°C T _{vi} =25°C	-	0.71	-	_
				$T_{vj}=25$ C $T_{vj}=125$ °C	-	0.30	-	-
	Reverse recovery time	t _{rr}		$T_{vj} = 120 \text{ C}$ $T_{vj} = 150^{\circ}\text{C}$	-	0.46	-	-
				T _{vi} =175°C		0.55	-	-

(*1) Turn on time $(t_{on}) = t_{d(on)} + t_r$, Turn off time $(t_{off}) = t_{d(off)} + t_f$



	Items	Symbols		Conditi	onc	Ch	aracterist	ics	Units
	nems	Symbols		Conditi		min.	typ.	max.	Units
				900V	T _{vj} =25°C	-	152	-	
		E _{on}	$I_{\rm C}, I_{\rm F} =$	= 600A	T _{vj} =125°C	-	194	-	
			$V_{\rm GE} =$	+15/-15 V	T _{vj} =150°C	-	212	-	
				±1Ω		-	241	-	
	Switching loss (per pulse)		$L_{\rm S} =$	35 nH	T _{vj} =25°C	-	140	-	
ter					T _{vj} =125°C	-	177	-	
Inverter		E_{off}			T _{vj} =150°C	-	188	-	mJ
드					T _{vj} =175°C	-	198	-	
					T _{vj} =25°C	-	70	-	
		Err			<i>T</i> _{vj} =125°C	-	128	-	
		← rr			T _{vj} =150°C	-	143	-	
					T _{vj} =175°C	-	165	-	
tor	Resistance	R	<i>T</i> =	25°C		-	5000	-	Ω
nis			<i>T</i> =	100°C		465	495	520	32
Thermistor	B value	В	<i>T</i> =	25/ 50°C		3305	3375	3450	К

■ Electrical characteristics (at *T*_{vj}= 25°C unless otherwise specified)

NOTICE:

The external gate resistance (R_G) shown above is one of our recommended value for the purpose of minimum switching loss. However the optimum R_G depends on circuit configuration and/or environment. We recommend that the R_G has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

Thermal resistance characteristics

Items	Symbols	Conditions	Ch	Units			
Items	Symbols	Conditions	min.	typ.	max.	Units	
Thermal resistance junction to	P	Inverter IGBT	-	-	0.039		
case(1 device)	$R_{\mathrm{th(j-c)}}$	Inverter FWD	-	-	0.055	к/w	
Thermal resistance case to	$R_{\rm th(c-s)}$	with 1 W/(m·K) thermal grease	_	0.0167	_	1.0.00	
heatsink(1 IGBT+1 FWD) (*1)	th(c-s)	with i w/(in-it) theimal grease	-	0.0107	-		

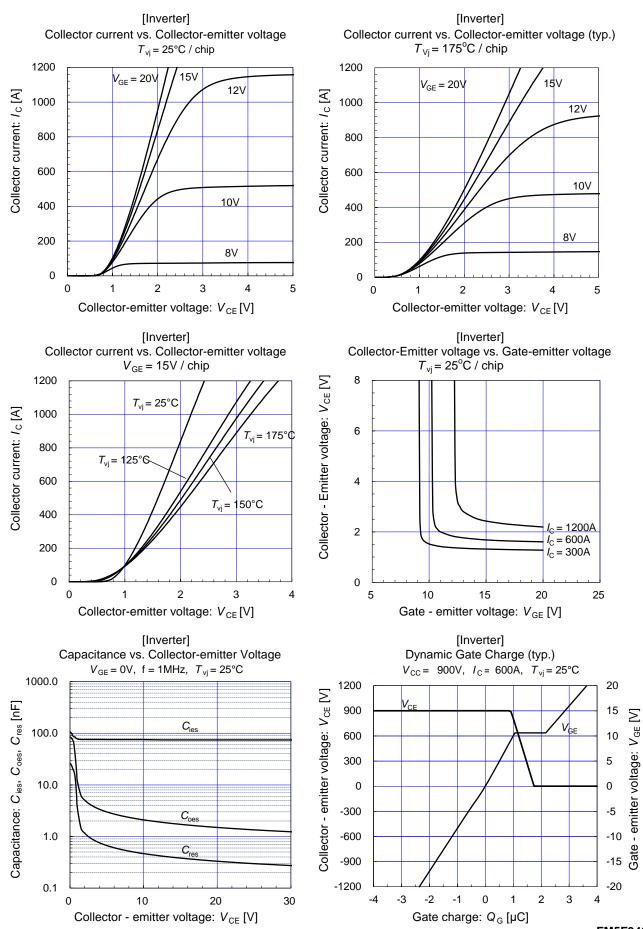
(*1) This is the value which is defined mounting on the additional heatsink with thermal grease.



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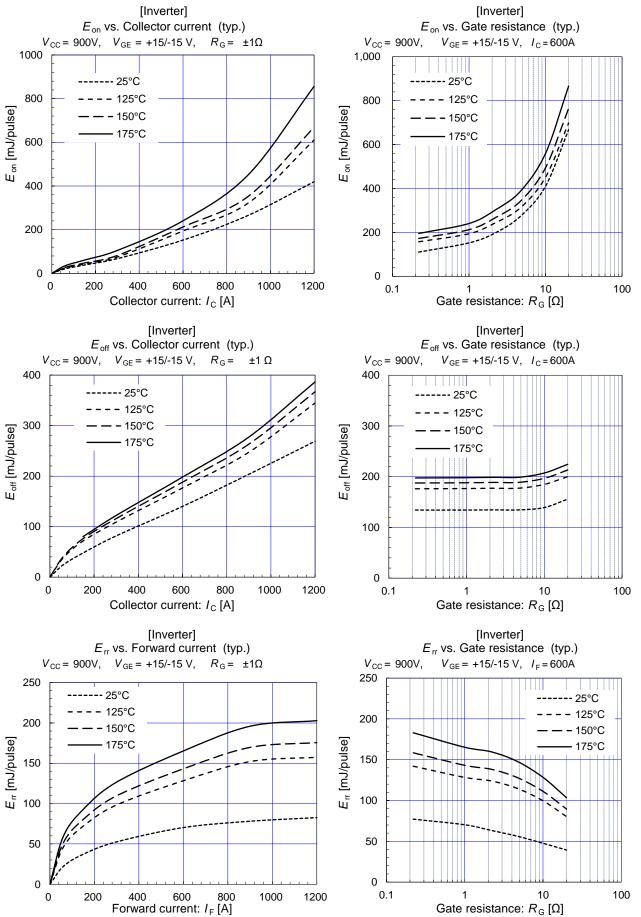
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FM5F9404 2019/05





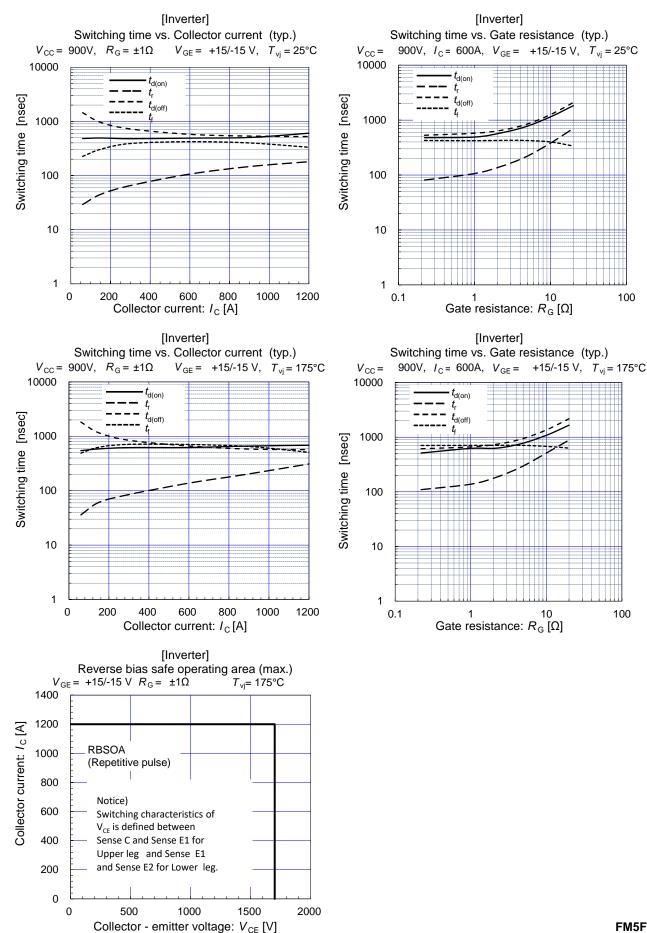
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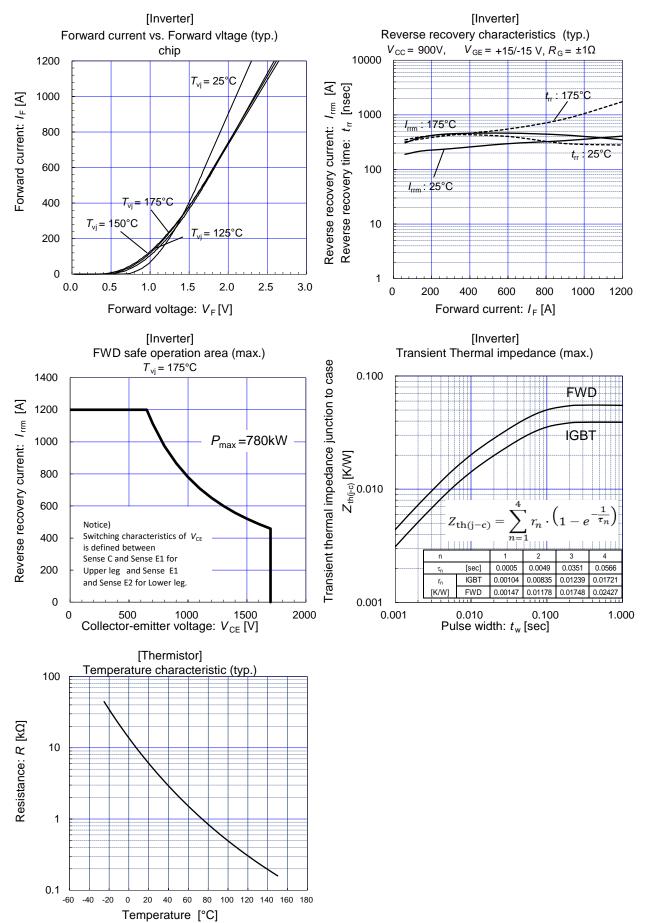
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