

7MBR75XZA120-50

IGBT Modules

Power Module(X series)
1200V / 75A / PIM

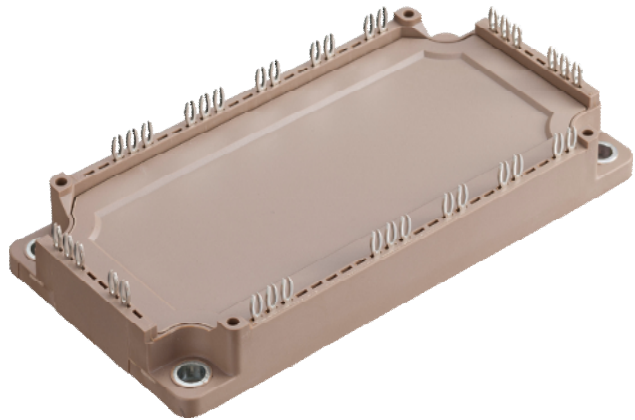
■ Features

- Low $V_{CE(sat)}$
- Compact Package
- P.C.Board Mount Module
- Converter Diode Bridge Dynamic Brake Circuit
- RoHS compliant Product

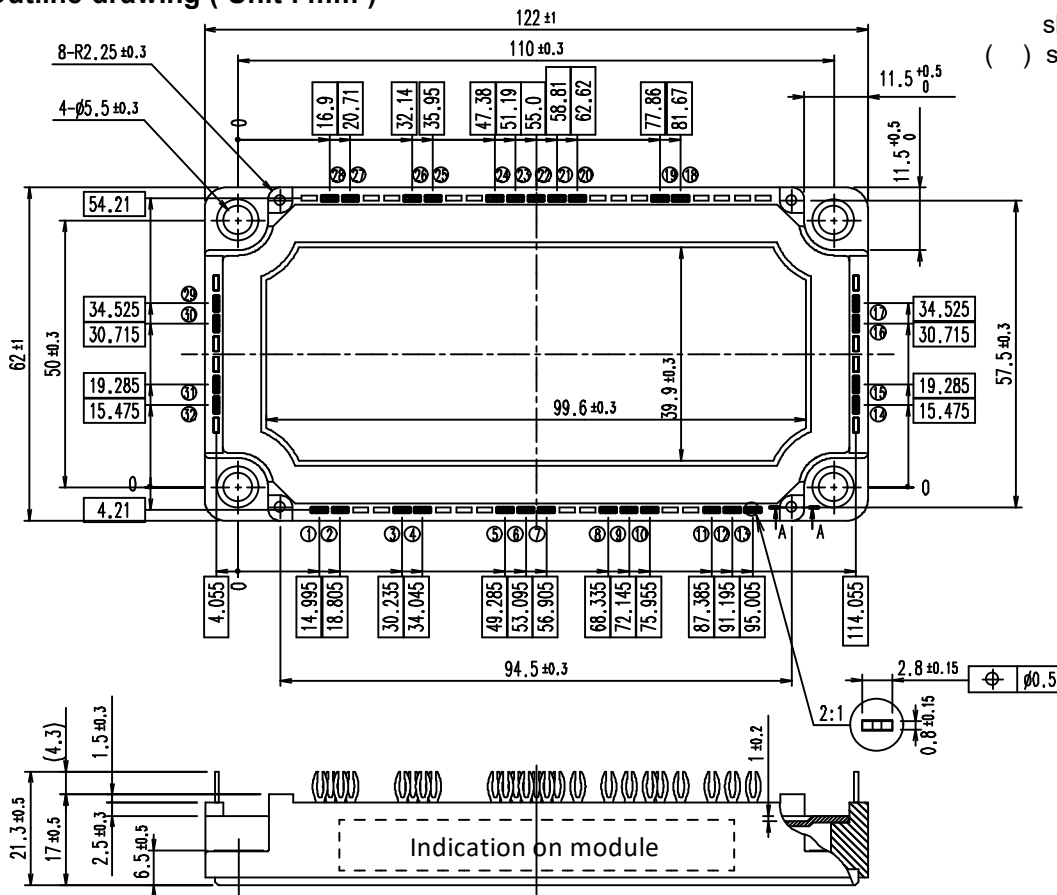
■ Applications

- Inverter for Motor Drive
- AC and DC Servo Drive Amplifier
- Uninterrupted Power Supply

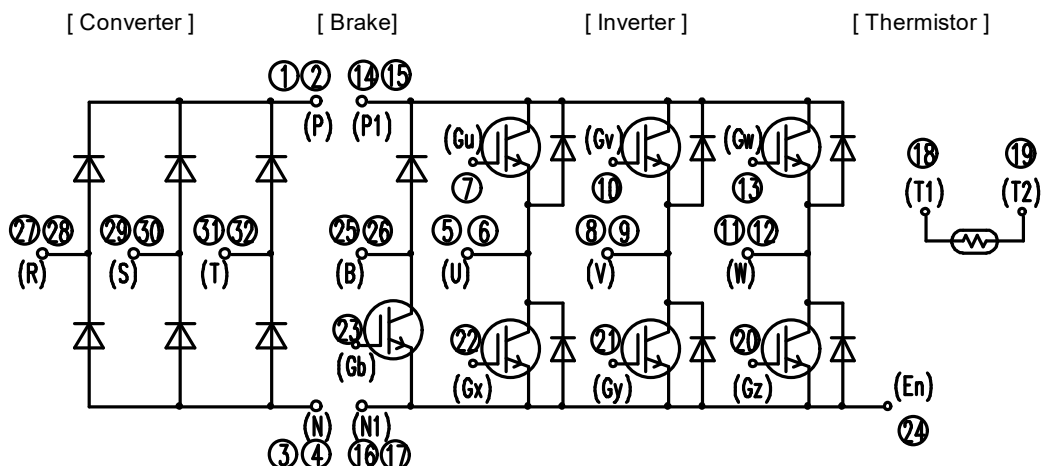
■ Typical appearance



■ Outline drawing (Unit : mm)



■ Equivalent circuit



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IGBT Modules
■ Maximum ratings (at $T_c = 25^\circ\text{C}$ unless otherwise specified)

Items			Symbols	Conditions		Maximum ratings	Units
Inverter	Collector-Emitter voltage		V_{CES}			1200	V
	Gate-Emitter voltage		V_{GES}			±20	V
	Collector current		I_{C}	Continuous	$T_{\text{c}}=100^{\circ}\text{C}$	75	A
			I_{C} pulse	1ms		150	
	Forward current		I_{F}	Continuous		75	
			I_{F} pulse	1ms		150	
Collector power dissipation		P_{C}	1 device		335	W	
Brake IGBT	Collector-Emitter voltage		V_{CES}			1200	V
	Gate-Emitter voltage		V_{GES}			±20	V
	Collector current		I_{C}	Continuous	$T_{\text{c}}=100^{\circ}\text{C}$	50	A
			I_{C} pulse	1ms		100	
	Collector power dissipation		P_{C}	1 device		250	W
Brake FWD	Forward current		I_{F}	Continuous		25	A
			I_{FRM}	1ms		50	
	Repetitive peak reverse voltage		V_{RRM}			1200	V
Converter	Repetitive peak reverse voltage		V_{RRM}			1600	V
	Average output current		I_{O}	Three-phase full wave rectified current	$T_{\text{c}}=80^{\circ}\text{C}$	75	A
	Surge current (Non-Repetitive) (*1)		I_{FSM}	$t=10\text{ms}$, Half sine wave form	$T_{\text{vj}}=25^{\circ}\text{C}$	630	A
					$T_{\text{vj}}=150^{\circ}\text{C}$	520	
		I^2t (Non-Repetitive) (*1)		I^2t		$T_{\text{vj}}=25^{\circ}\text{C}$	2000
					$T_{\text{vj}}=150^{\circ}\text{C}$	1350	
Junction temperature			T_{vj}	Inverter, Brake		175	$^{\circ}\text{C}$
				Converter		150	
Operating junction temperature (under switching conditions)			T_{vjop}	Inverter, Brake		175	
				Converter		150	
Case temperature			T_{c}			125	
Storage temperature			T_{stg}			-40 ~ 125	
Isolation voltage	between terminals and copper base (*2)		V_{isol}	A.C. : 1min.		2500	Vrms
	between thermistor and others (*3)						
Screw torque	Mounting torque of screws to heat sink		M_{s}	M5		2.5~6.0	N·m

 (*1) T_{vj} : Temperature at test start.

(*2) All terminals should be connected together during the test.

(*3) Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

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IGBT Modules
■ Electrical characteristics (at $T_{vj} = 25^{\circ}\text{C}$ unless otherwise specified)

Items		Symbols	Conditions		Characteristics			Units
					min.	typ.	max.	
Inverter	Zero gate voltage collector current	I_{CES}	$V_{GE} = 0V$ $V_{CE} = 1200V$		-	-	50	μA
	Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0V \quad V_{GE} = +20/-20V$		-	-	100	nA
	Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V$ $I_C = 75mA$		6.0	6.5	7.0	V
	Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal)	$V_{GE} = 15V$ $I_C = 75A$	$T_{vj}=25^{\circ}C$	-	1.70	2.20	V
		$V_{CE(sat)}$ (chip)		$T_{vj}=25^{\circ}C$	-	1.50	1.95	
				$T_{vj}=125^{\circ}C$	-	1.85	-	
				$T_{vj}=150^{\circ}C$	-	1.95	-	
				$T_{vj}=175^{\circ}C$	-	2.00	-	
	Internal gate resistance	r_g	-		-	7.5	-	Ω
	Capacitance	C_{ies}	$V_{CE} = 10V, V_{GE} = 0V, f = 1MHz$		-	8.0	-	nF
		C_{oes}			-	0.27	-	
		C_{res}			-	0.07	-	
	Gate charge	Q_G	$V_{CC} = 600V \quad V_{GE} = -15 \rightarrow +15V$ $I_C = 75A$		-	510	-	nC
	Forward voltage	V_F (terminal)	$I_F = 75A$	$T_{vj}=25^{\circ}C$	-	2.00	2.50	V
		V_F (chip)		$T_{vj}=25^{\circ}C$	-	1.80	2.25	
				$T_{vj}=125^{\circ}C$	-	1.85	-	
				$T_{vj}=150^{\circ}C$	-	1.80	-	
$T_{vj}=175^{\circ}C$				-	1.75	-		
Switching time (*1)	$t_{d(on)}$	$V_{CC} = 600V$ $I_C, I_F = 75A \quad L_s = 30nH$ $V_{GE} = +15/-15V$ $R_G = 5.1\Omega$	$T_{vj}=25^{\circ}C$	-	0.18	-	μs	
			$T_{vj}=125^{\circ}C$	-	0.21	-		
			$T_{vj}=150^{\circ}C$	-	0.21	-		
			$T_{vj}=175^{\circ}C$	-	0.22	-		
	t_r	$V_{CC} = 600V$ $I_C, I_F = 75A \quad L_s = 30nH$ $V_{GE} = +15/-15V$ $R_G = 5.1\Omega$	$T_{vj}=25^{\circ}C$	-	0.04	-		
			$T_{vj}=125^{\circ}C$	-	0.05	-		
			$T_{vj}=150^{\circ}C$	-	0.05	-		
			$T_{vj}=175^{\circ}C$	-	0.05	-		
	$t_{d(off)}$	$V_{CC} = 600V$ $I_C, I_F = 75A \quad L_s = 30nH$ $V_{GE} = +15/-15V$ $R_G = 5.1\Omega$	$T_{vj}=25^{\circ}C$	-	0.25	-		
			$T_{vj}=125^{\circ}C$	-	0.28	-		
			$T_{vj}=150^{\circ}C$	-	0.29	-		
			$T_{vj}=175^{\circ}C$	-	0.29	-		
	t_f	$V_{CC} = 600V$ $I_C, I_F = 75A \quad L_s = 30nH$ $V_{GE} = +15/-15V$ $R_G = 5.1\Omega$	$T_{vj}=25^{\circ}C$	-	0.12	-		
			$T_{vj}=125^{\circ}C$	-	0.18	-		
			$T_{vj}=150^{\circ}C$	-	0.20	-		
			$T_{vj}=175^{\circ}C$	-	0.21	-		
Reverse recovery time	t_{rr}	$V_{CC} = 600V$ $I_C, I_F = 75A \quad L_s = 30nH$ $V_{GE} = +15/-15V$ $R_G = 5.1\Omega$	$T_{vj}=25^{\circ}C$	-	0.10	-		
			$T_{vj}=125^{\circ}C$	-	0.17	-		
			$T_{vj}=150^{\circ}C$	-	0.21	-		
			$T_{vj}=175^{\circ}C$	-	0.24	-		

(*1) Turn on time (t_{on}) = $t_{d(on)} + t_r$, Turn off time (t_{off}) = $t_{d(off)} + t_f$

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Items		Symbols	Conditions		Characteristics			Units
					min.	typ.	max.	
Inverter	Switching loss (per pulse)	E_{on}	$V_{CC} = 600V$	$T_{vj}=25^{\circ}C$	-	5.41	-	mJ
			$I_C, I_F = 75A \quad L_s = 30nH$	$T_{vj}=125^{\circ}C$	-	7.80	-	
			$V_{GE} = +15/-15 V$	$T_{vj}=150^{\circ}C$	-	8.48	-	
			$R_G = 5.1 \Omega$	$T_{vj}=175^{\circ}C$	-	9.27	-	
		E_{off}	$V_{CC} = 600V$	$T_{vj}=25^{\circ}C$	-	5.53	-	
			$I_C, I_F = 75A \quad L_s = 30nH$	$T_{vj}=125^{\circ}C$	-	6.64	-	
			$V_{GE} = +15/-15 V$	$T_{vj}=150^{\circ}C$	-	7.23	-	
			$R_G = 5.1 \Omega$	$T_{vj}=175^{\circ}C$	-	7.48	-	
		E_{rr}	$V_{CC} = 600V$	$T_{vj}=25^{\circ}C$	-	2.20	-	
			$I_C, I_F = 75A \quad L_s = 30nH$	$T_{vj}=125^{\circ}C$	-	3.88	-	
			$V_{GE} = +15/-15 V$	$T_{vj}=150^{\circ}C$	-	4.75	-	
			$R_G = 5.1 \Omega$	$T_{vj}=175^{\circ}C$	-	5.33	-	
Zero gate voltage collector current	I_{CES}	$V_{GE} = 0V$ $V_{CE} = 1200V$		-	-	50	μA	
Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0V, \quad V_{GE} = +20/-20V$		-	-	100	nA	
Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal)	$V_{GE} = 15V$ $I_C = 50A$	$T_{vj}=25^{\circ}C$	-	1.65	2.10	V	
	$V_{CE(sat)}$ (chip)		$T_{vj}=25^{\circ}C$	-	1.50	1.95		
			$T_{vj}=125^{\circ}C$	-	1.85	-		
			$T_{vj}=150^{\circ}C$	-	1.95	-		
			$T_{vj}=175^{\circ}C$	-	2.00	-		
Internal gate resistance	r_g	-		-	0	-	Ω	
Brake	Switching time (*1)	$t_{d(on)}$	$V_{CC} = 600V$	$T_{vj}=25^{\circ}C$	-	0.09	-	μs
			$I_C = 50A \quad L_s = 30nH$	$T_{vj}=125^{\circ}C$	-	0.09	-	
			$V_{GE} = +15/-15 V$	$T_{vj}=150^{\circ}C$	-	0.09	-	
			$R_G = 18 \Omega$	$T_{vj}=175^{\circ}C$	-	0.10	-	
		t_r	$V_{CC} = 600V$	$T_{vj}=25^{\circ}C$	-	0.04	-	
			$I_C = 50A \quad L_s = 30nH$	$T_{vj}=125^{\circ}C$	-	0.04	-	
			$V_{GE} = +15/-15 V$	$T_{vj}=150^{\circ}C$	-	0.04	-	
			$R_G = 18 \Omega$	$T_{vj}=175^{\circ}C$	-	0.04	-	
		$t_{d(off)}$	$V_{CC} = 600V$	$T_{vj}=25^{\circ}C$	-	0.24	-	
			$I_C = 50A \quad L_s = 30nH$	$T_{vj}=125^{\circ}C$	-	0.27	-	
			$V_{GE} = +15/-15 V$	$T_{vj}=150^{\circ}C$	-	0.27	-	
			$R_G = 18 \Omega$	$T_{vj}=175^{\circ}C$	-	0.28	-	
		t_f	$V_{CC} = 600V$	$T_{vj}=25^{\circ}C$	-	0.11	-	
			$I_C = 50A \quad L_s = 30nH$	$T_{vj}=125^{\circ}C$	-	0.17	-	
			$V_{GE} = +15/-15 V$	$T_{vj}=150^{\circ}C$	-	0.20	-	
			$R_G = 18 \Omega$	$T_{vj}=175^{\circ}C$	-	0.21	-	
Reverse current	I_{RRM}	$V_R = 1200V$		-	-	50	μA	
Forward voltage	V_F (terminal)	$I_F = 25A$	$T_{vj}=25^{\circ}C$	-	1.95	2.40	V	
	V_F (chip)	$I_F = 25A$	$T_{vj}=25^{\circ}C$	-	1.80	2.25		
		$T_{vj}=125^{\circ}C$	-	1.85	-			
		$T_{vj}=150^{\circ}C$	-	1.80	-			
		$T_{vj}=175^{\circ}C$	-	1.75	-			
Reverse current	I_{RRM}	$V_R = 1600V$		-	-	50	μA	
Forward voltage	V_{FM}	$I_F = 75A$	terminal	-	1.35	1.85	V	
			chip	-	1.15	1.60		
Thermistor	Resistance	R	$T = 25^{\circ}C$		-	5000	-	Ω
			$T = 100^{\circ}C$		465	495	520	
	B value	B	$T = 25/ 50^{\circ}C$		3305	3375	3450	K

(*1) Turn on time (t_{on}) = $t_{d(on)} + t_r$, Turn off time (t_{off}) = $t_{d(off)} + t_f$

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NOTICE:

The external gate resistance (R_G) shown above is one of our recommended value for the purpose of minimum switching loss. However the optimum R_G depends on circuit configuration and/or environment. We recommend that the R_G has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

■ Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance (1device)	$R_{th(j-c)}$	Inverter IGBT	-	-	0.45	°C/W
		Inverter FWD	-	-	0.55	
		Brake IGBT	-	-	0.59	
		Brake FWD	-	-	1.00	
		Converter Diode	-	-	0.72	
Contact thermal resistance (1 IGBT+1 FWD) (*1)	$R_{th(c-f)}$	with 1 W/(m·K) thermal grease	-	0.05	-	

(*1) This is the value which is defined mounting on the additional cooling fin with thermal grease.

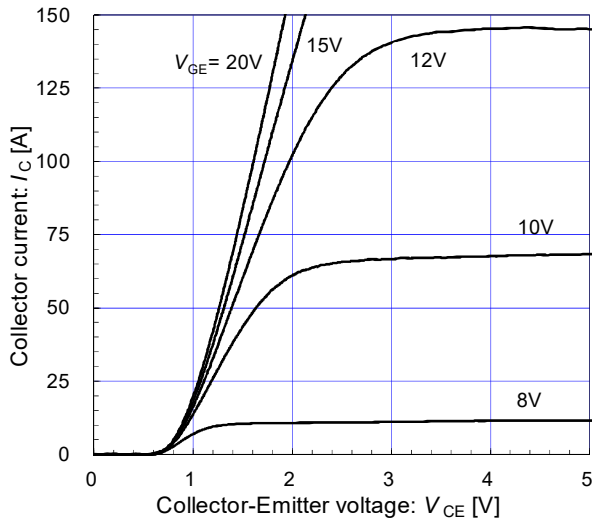
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IGBT Modules

[Inverter]

Collector current vs. Collector-Emittor voltage (typ.)

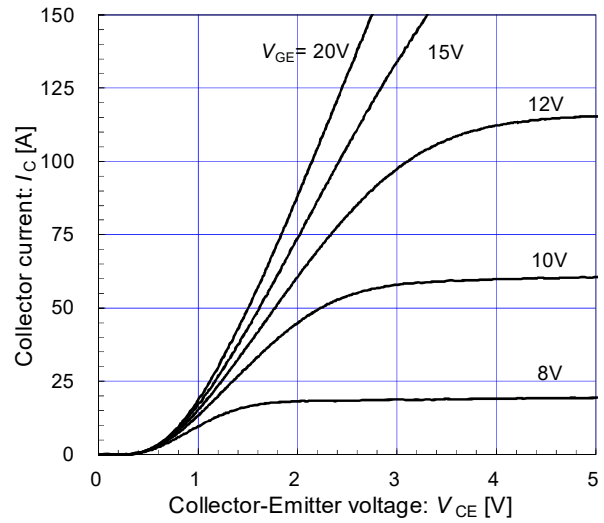
$T_{vj} = 25^{\circ}\text{C} / \text{chip}$



[Inverter]

Collector current vs. Collector-Emittor voltage (typ.)

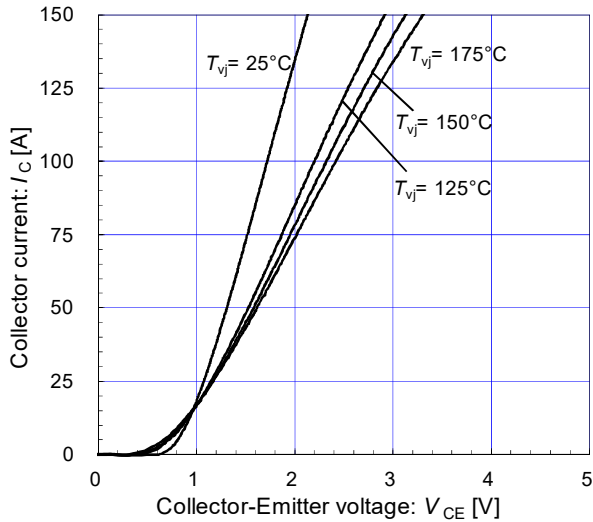
$T_{vj} = 175^{\circ}\text{C} / \text{chip}$



[Inverter]

Collector current vs. Collector-Emittor voltage (typ.)

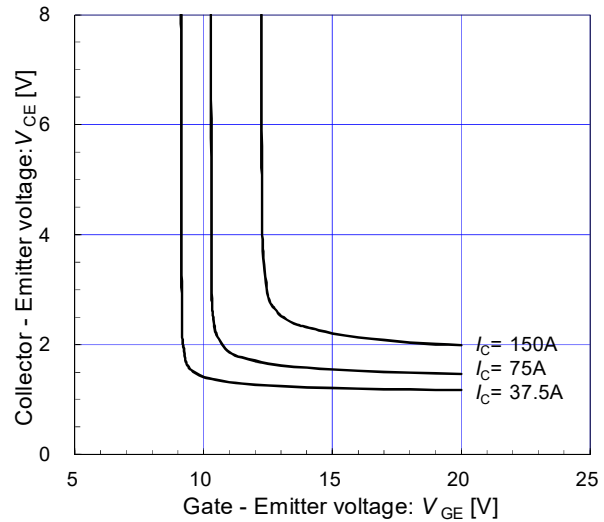
$V_{GE} = 15\text{V} / \text{chip}$



[Inverter]

Collector-Emittor voltage vs. Gate-Emittor voltage (typ.)

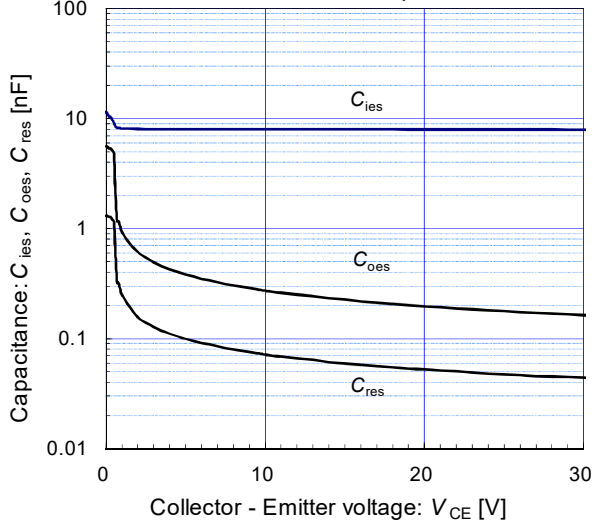
$T_{vj} = 25^{\circ}\text{C} / \text{chip}$



[Inverter]

Capacitance vs. Collector-Emittor voltage (typ.)

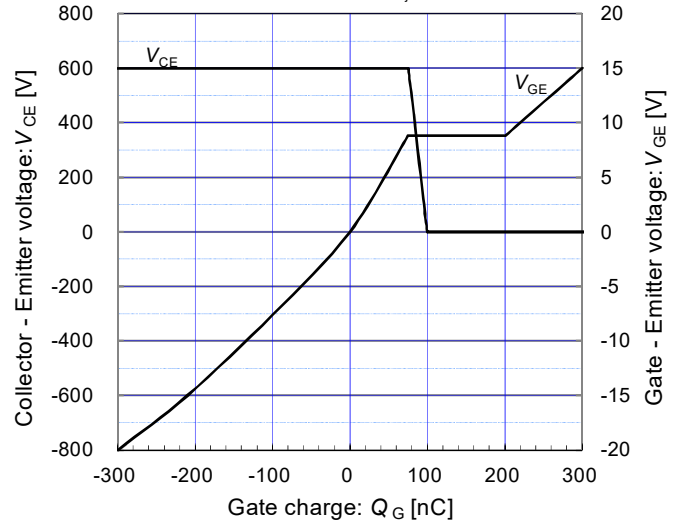
$V_{GE} = 0\text{V}, f = 1\text{MHz}, T_{vj} = 25^{\circ}\text{C}$



[Inverter]

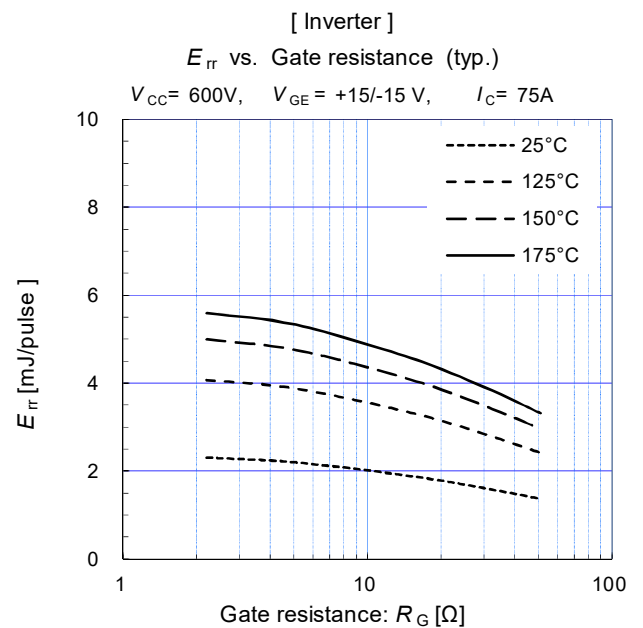
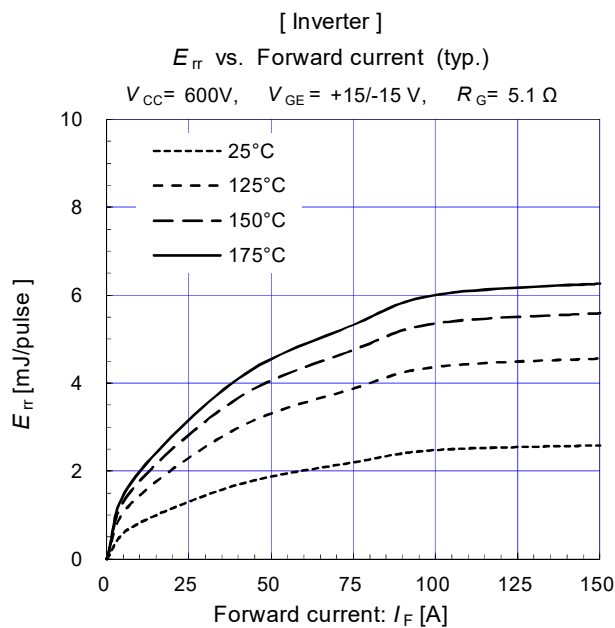
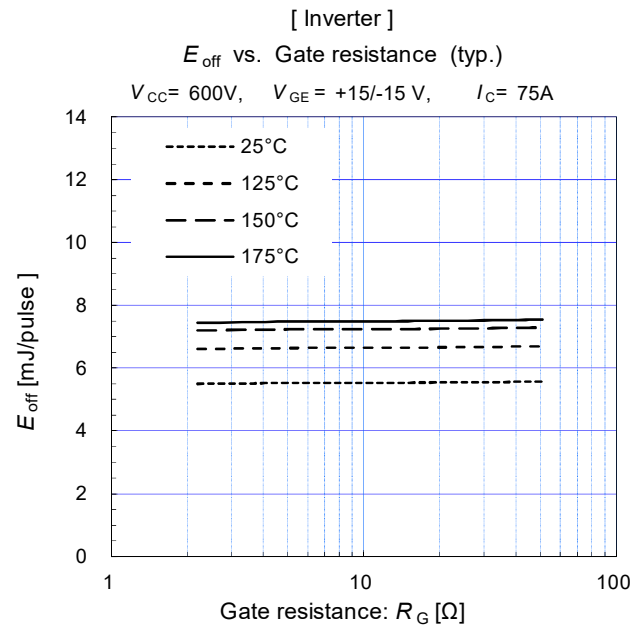
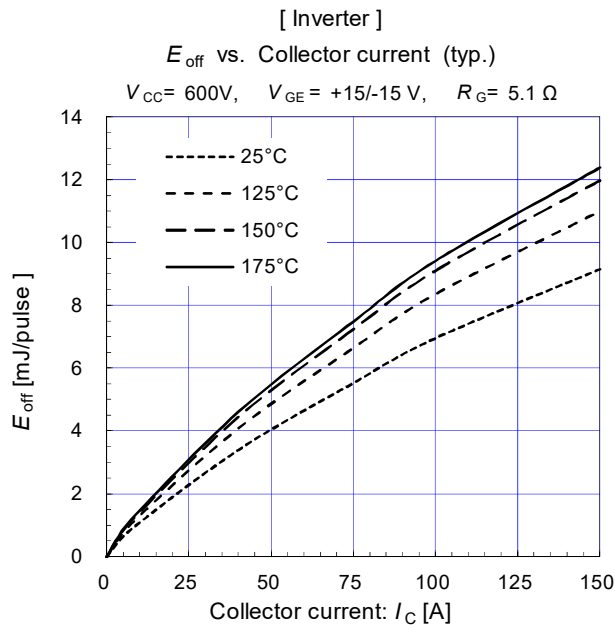
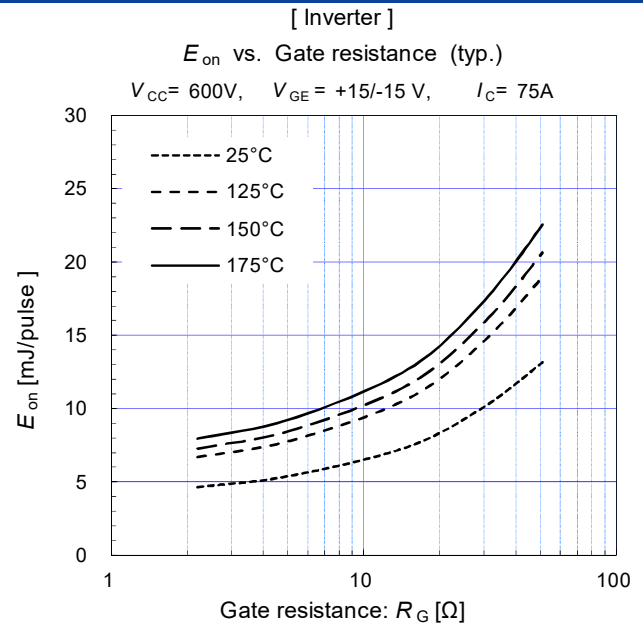
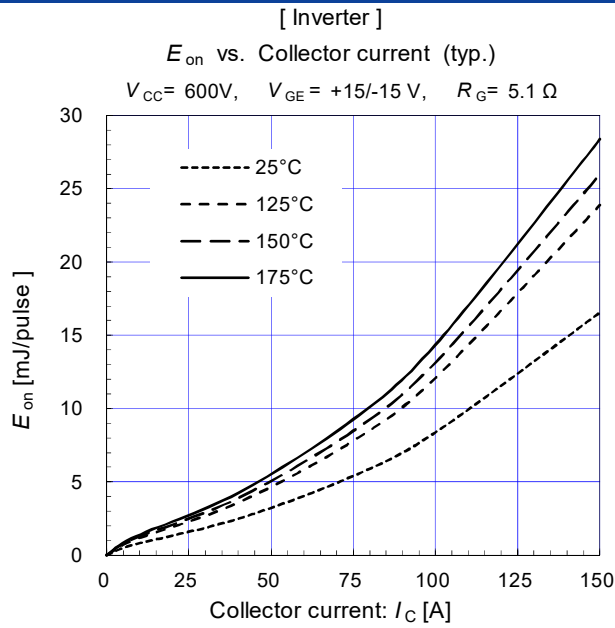
Dynamic Gate charge (typ.)

$V_{CC} = 600\text{V}, I_C = 75\text{A}, T_{vj} = 25^{\circ}\text{C}$



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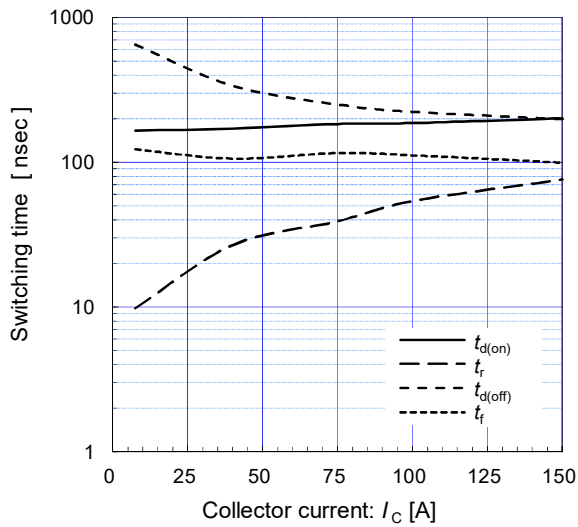
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IGBT Modules

[Inverter]

Switching time vs. Collector current (typ.)

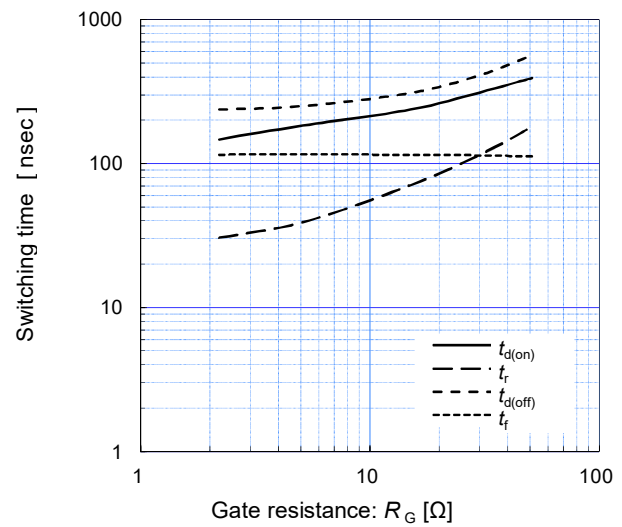
$V_{CC}=600V$, $R_G=5.1\Omega$, $V_{GE}=+15/-15V$, $T_{vj}=25^\circ C$



[Inverter]

Switching time vs. Gate resistance (typ.)

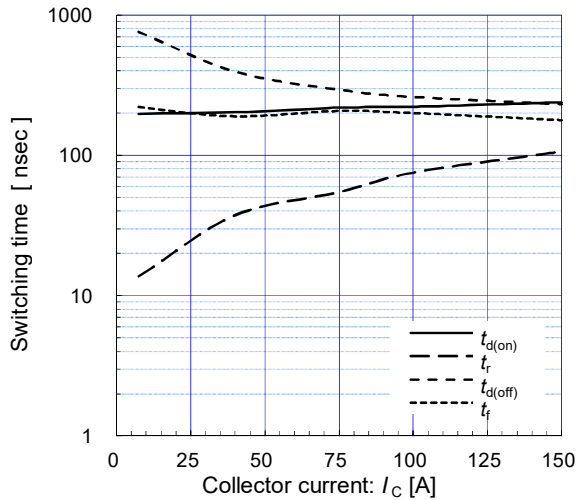
$V_{CC}=600V$, $I_C=75A$, $V_{GE}=+15/-15V$, $T_{vj}=25^\circ C$



[Inverter]

Switching time vs. Collector current (typ.)

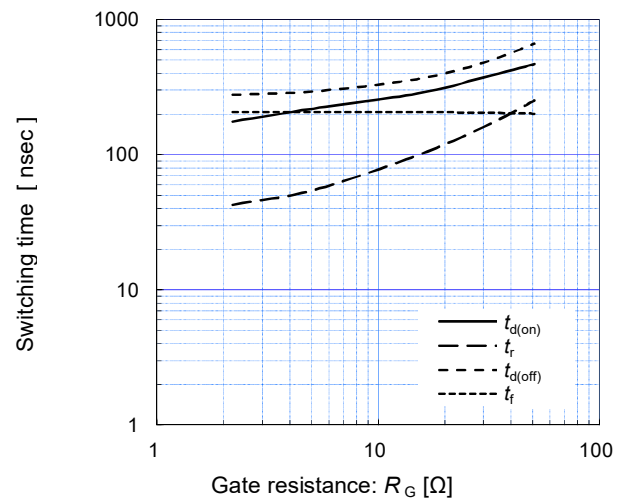
$V_{CC}=600V$, $R_G=5.1\Omega$, $V_{GE}=+15/-15V$, $T_{vj}=175^\circ C$



[Inverter]

Switching time vs. Gate resistance (typ.)

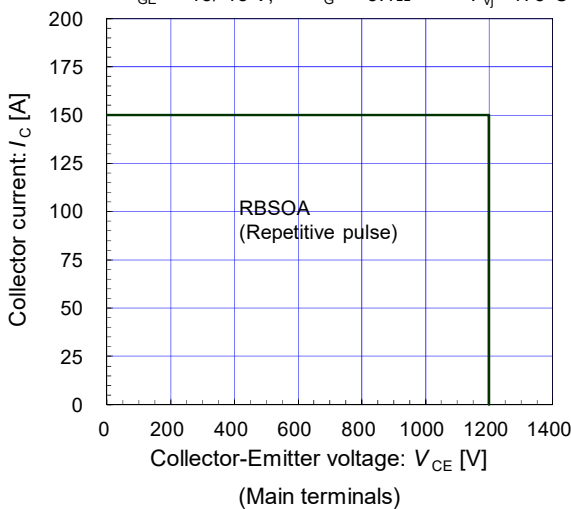
$V_{CC}=600V$, $I_C=75A$, $V_{GE}=+15/-15V$, $T_{vj}=175^\circ C$



[Inverter]

Reverse bias safe operating area (max.)

$V_{GE}=+15/-15V$, $R_G \geq 5.1\Omega$, $T_{vj}=175^\circ C$

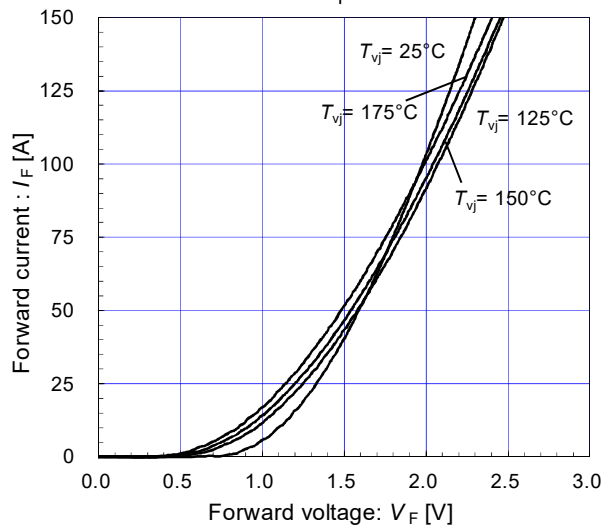


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[Inverter]

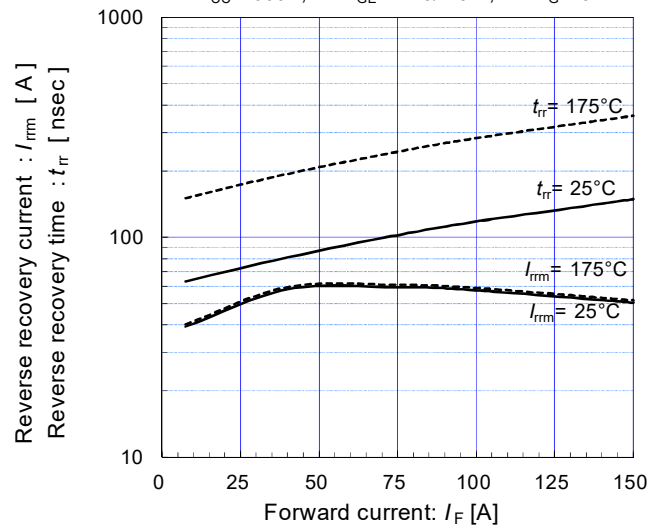
Forward current vs. Forward voltage (typ.)
chip



[Inverter]

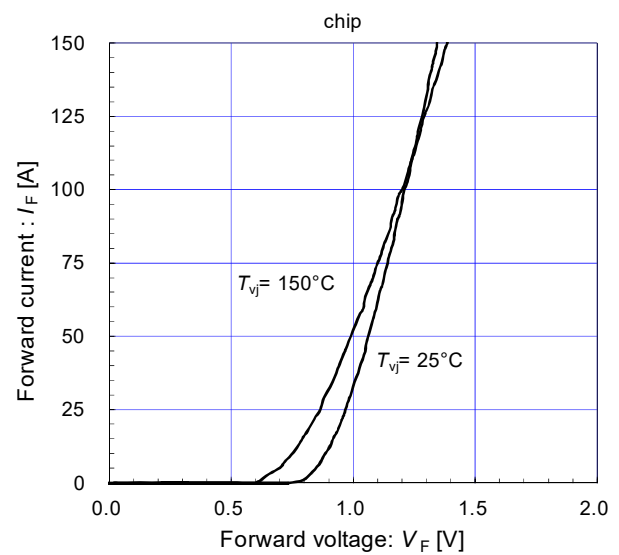
Reverse recovery characteristics (typ.)

$V_{CC} = 600V$, $V_{GE} = +15/-15V$, $R_G = 5.1\Omega$

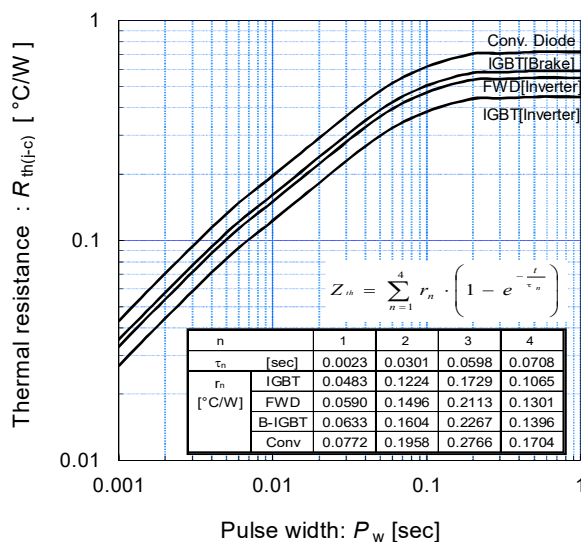


[Converter]

Forward current vs. Forward voltage (typ.)

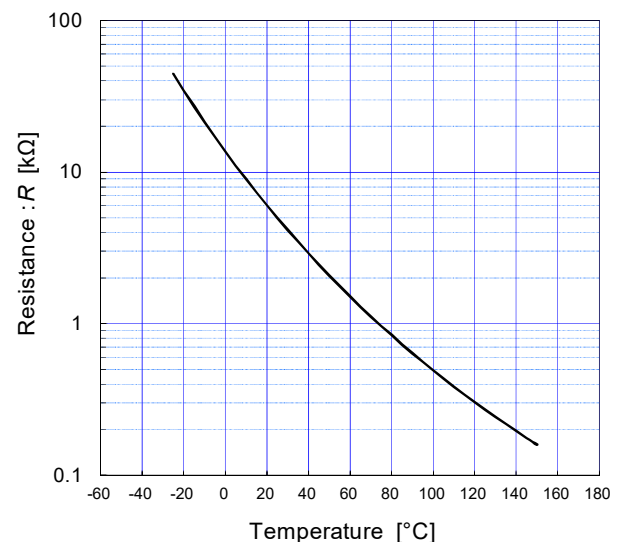


Transient thermal resistance (max.)



[Thermistor]

Temperature characteristic (typ.)



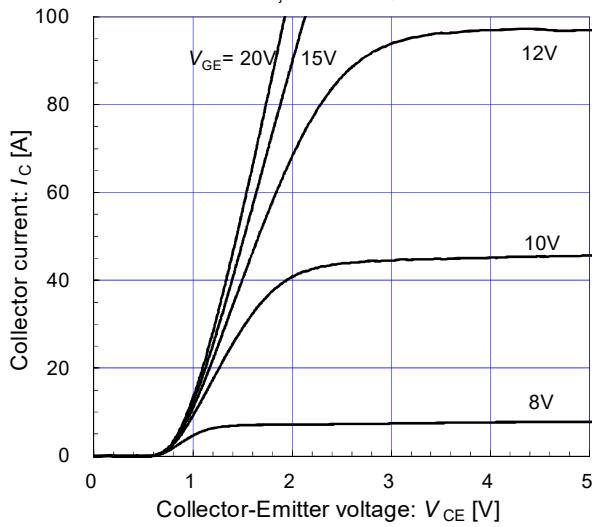
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[Brake]

Collector current vs. Collector-Emmitter voltage (typ.)

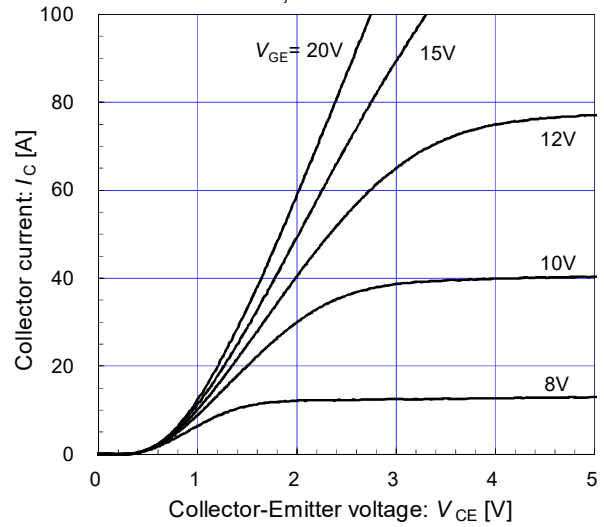
$T_{vj} = 25^{\circ}\text{C} / \text{chip}$



[Brake]

Collector current vs. Collector-Emmitter voltage (typ.)

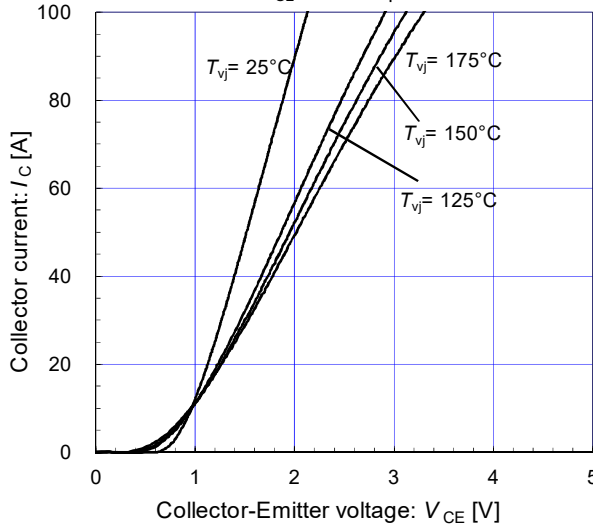
$T_{vj} = 175^{\circ}\text{C} / \text{chip}$



[Brake]

Collector current vs. Collector-Emmitter voltage (typ.)

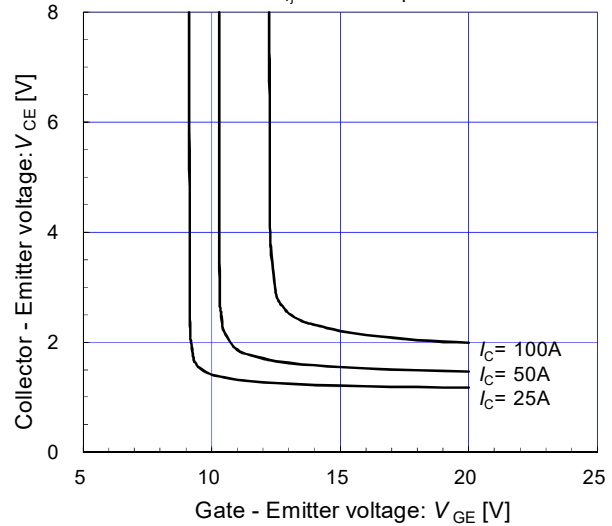
$V_{GE} = 15\text{V} / \text{chip}$



[Brake]

Collector-Emmitter voltage vs. Gate-Emmitter voltage (typ.)

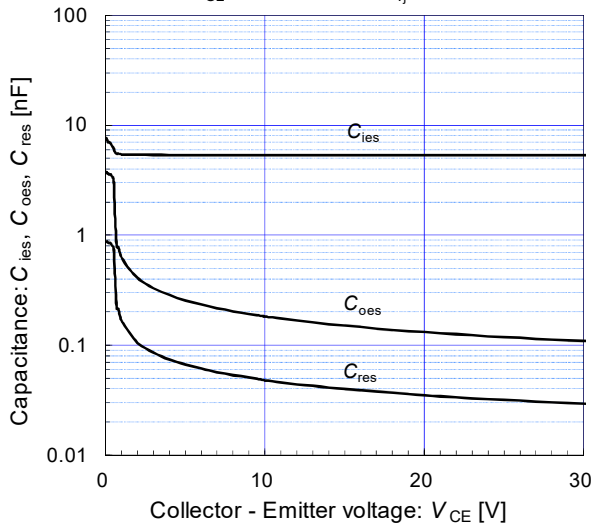
$T_{vj} = 25^{\circ}\text{C} / \text{chip}$



[Brake]

Capacitance vs. Collector-Emmitter voltage (typ.)

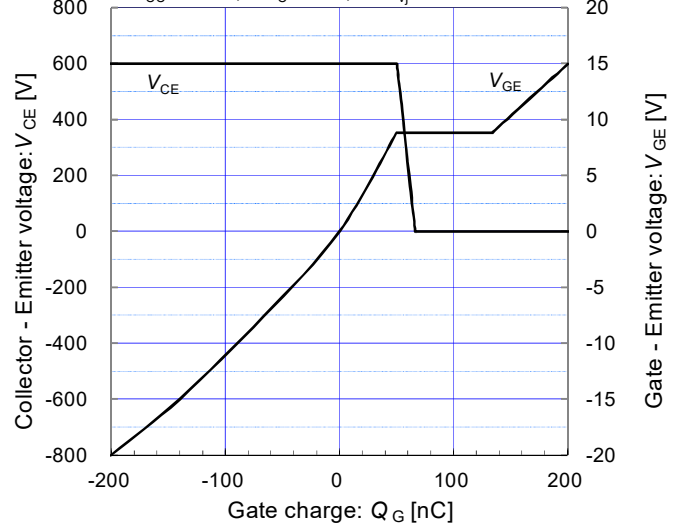
$V_{GE} = 0\text{V}, f = 1\text{MHz}, T_{vj} = 25^{\circ}\text{C}$



[Brake]

Dynamic gate charge (typ.)

$V_{CC} = 600\text{V}, I_C = 50\text{A}, T_{vj} = 25^{\circ}\text{C}$



FM6M01814b

2023/3

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